

Diagram illustrating the 6-channel multiplexer circuit. The circuit consists of a 6-channel multiplexer block with inputs labeled WHRD-e1, WHRD-e5-1, WH-I1-N, WH-a1-6, WH-K12.7-4, BK-I1-L1, WH-I2.1-1, and WH-a1-3. The outputs are labeled a1 through a6.

e3-rinse-add.

N

S

RD-12.6-2

RD-12.6-1

PE

4 GNYE-PE-1

2 GNYE-r1

1 GNYE-m2

 GNYE-PE-4

Diagram illustrating a 16-bit bus structure. The bus is labeled 16 at the top. The bus contains nine numbered boxes (1-9) and three connection points (6, 8, 9). The connections are as follows:

- Box 6 is connected to GY-A1 and WH-I2.4-4.
- Box 8 is connected to BU-f1-4 and WH-I2.4-2.
- Box 9 is connected to VT-s2 and WH-I2.4-1.

upper rack valve

A1

GY-16-6

WH-15-3

WH-e6-1

addition device

A2

YE-12.2-1

YE-12.2-2

e1 — WHRD-12.1-2
 WHRD-a1-1
 4 — WH-12.2-6
 1 — WHVT-m2-2
 2

RD-f1-1
WH-A1
GYRD-m3-L1
WH-s2

$f_5 + f_2 - f_4$

NTC

$f_2 - f_4$

GN-15-5

GN-15-6

f_5

85°

RD-15-1

RD-r1

The diagram shows a 2:1 MUX with two data inputs and one select input. The inputs are labeled RD-r1, WHRD-a1-1, and WHRD-m3. The output is labeled e5. The MUX is represented by a trapezoidal symbol with a diagonal line and a circle in the center.

L1 ——— BK-a1-4
N ——— WH-a1-2
PE-3 ——— GNYE-r1

① exist according to equipment



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